

Appl. No. 09/922,141  
Amendment and/or Response  
Reply to Office action of May 7, 2004

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**Amendments to the Specification**

Kindly delete the abstract and replace same with the following abstract:

A circuit arrangement includes at least one adapter circuit (10), which amplifies an analog input signal of a low current ( $I_i$ ) by an amplification factor ( $n$ ) into a particularly analog output signal of a higher current ( $I_o$ ). The adapter circuit includes an input, which corresponds to a range of low voltages ( $U_i$ ); an output (18), which corresponds to a range of higher voltages ( $U_o$ ); at least one npn transistor current mirror (14); and at least one pnp transistor current mirror (16) arranged in series with the npn transistor current mirror (14), and connected to at least one high voltage source (30), wherein the at least one pnp transistor current mirror amplifies the input signal, which is received from the at least one npn transistor current mirror.

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